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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known				
Application Number	10/622,498-Conf. #4995			
Filing Date	July 21, 2003			
First Named Inventor	Tadashi lwasaki			
Art Unit	2819			
Examiner Name	C. L. Hammond			
Attorney Docket Number	N0029.1644			

	U.S. PATENT DOCUMENTS				
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	

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	NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²		
	CA	"A 660 MB/s Interface Megacell Portable Circuit in 0.3 μm-0.7 μm CMOS ASIC", IEEE Journal of Solid-State Circuits, Vol. 31, No. 12, December 1996.			
	СВ	"Design Guide For A Low Speed Buffer For The Universal Serial Bus", Revision 1.1 December, 1996 Intel Corporation.			

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